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PATENT
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Assistant Commissioner for Patents
Washington, D.C. 20231

By: Sherry Barton

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

MO, BRIAN SZE-KI, et al.

Application No.: Not yet assigned

For: METHOD OF FORMING A
TRENCH TRANSISTOR HAVING A
SUPERIOR GATE DIELECTRIC

Examiner: Not yet assigned

Art Unit: Not yet assigned

PRELIMINARY AMENDMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Prior to examination of the above-referenced application, please enter the following amendments and remarks.

IN THE TITLE:

Please amend the title as follows: --METHOD OF FORMING A
TRENCH TRANSISTOR HAVING A SUPERIOR GATE DIELECTRIC--.

A1